

Notice of References Cited	Application/Control No.	Applicant(s)/Patent Under Reexamination	
	10/041,791	HIRANO ET AL.	
	Examiner	Art Unit	Page 1 of 1
	Richard A. Booth	2812	

U.S. PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
A	US-6,165,841	12-2000	Kim et al.	438/255
B	US-2002/0024086	02-2002	Yates et al.	257/309
C	US-6,010,942	01-2000	Chien et al.	438/396
D	US-6,100,203	08-2000	Kil et al.	438/745
E	US-6,232,178	05-2001	Yamamoto, Ichiro	438/255
F	US-5,767,005	06-1998	Doan et al.	438/593
G	US-			
H	US-			
I	US-			
J	US-			
K	US-			
L	US-			
M	US-			

FOREIGN PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
N	JP2000174208A	06-2000	Japan	Yamazaki	-----
O	JP11274097A	10-1999	Japan	Sony Corp.	-----
P	JP08306646A	11-1996	Japan	Watanabe et al.	-----
Q					
R					
S					
T					

NON-PATENT DOCUMENTS

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)		
U	Ghandhi, "VLSI Fabrication Principles Silicon and Gallium Arsenide", John Wiley & Sons, 1994, pages 639-649.		
V			
W			
X			

*A copy of this reference is not being furnished with this Office action (See MPEP § 707.05(a))
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign